

# ADSL LINE TRANSFORMER

## FEATURES

- Matched to Texas Instruments TNETD4500C CO chipsets
- Excellent THD Performance



Electrical Specifications @ 25°C — Operating Temperature -40°C to 85°C

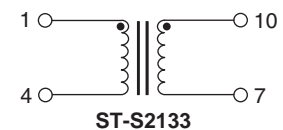
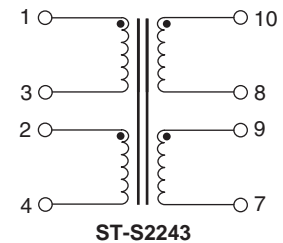
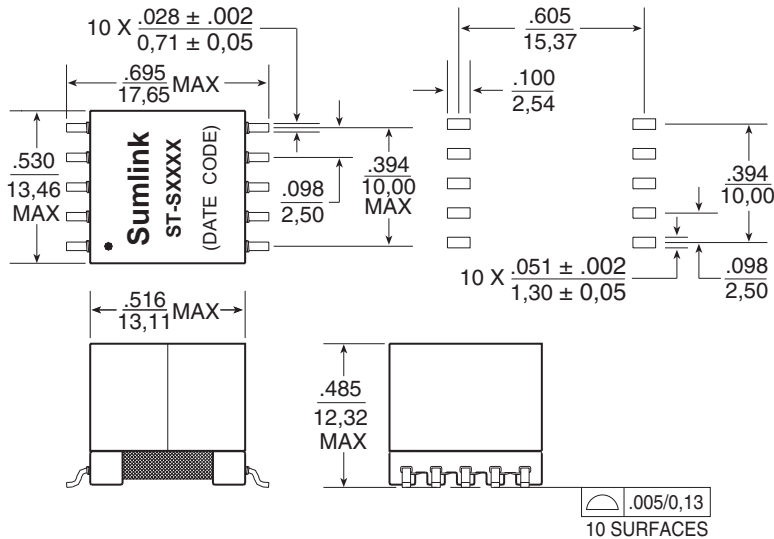
Part Number	Turns Ratio Ratio (Line:Chip)	OCL Primary ( $\mu\text{H} \pm 6\%$ )	Leakage Inductance ( $\mu\text{H} \text{ MAX}$ )	DCR Primary (1-3 & 2-4) ( $\Omega \text{ MAX}$ )	DCR Secondary (10-8 & 9-7) ( $\Omega \text{ MAX}$ )	Isolation Voltage ( $V_{\text{rms}}$ )	THD @ 100kHz, 5.0 Vp-p (dB MIN)
ST-S2243 <sup>1</sup>	2 : 1	400	5.0	0.5	0.3	1500	-80
ST-S2133 <sup>2</sup>	2 : 1	75	5.0	0.5	0.3	1500	-80

<sup>1</sup> ST-S2243 is designed for ADSL over POTS applications where THS6012/72 Line Drivers and Active termination is used.

<sup>2</sup> ST-S2133 is designed for ADSL over ISDN applications where THS6012/72 Line Drivers and Active termination is used.

## Mechanical

## Schematic



Dimensions:  $\frac{\text{Inches}}{\text{mm}}$   
Unless otherwise specified,  
all tolerances are  $\pm \frac{.010}{0,25}$