

ADSL LINE TRANSFORMER

FEATURES

- Matched to Alcatel Dynamite MTK20850 CO chipset and Tripath Line Driver
- Designed optimized for CO Applications
- Compact footprint for high density board layouts

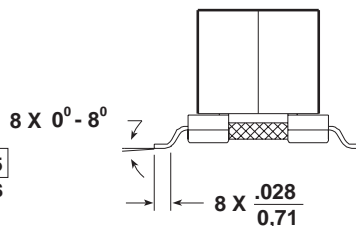
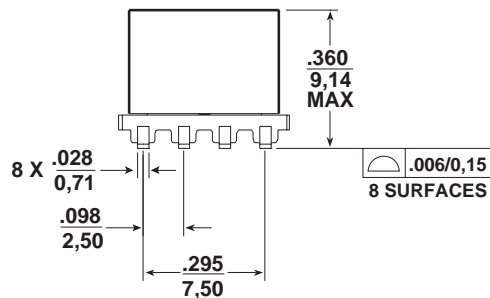
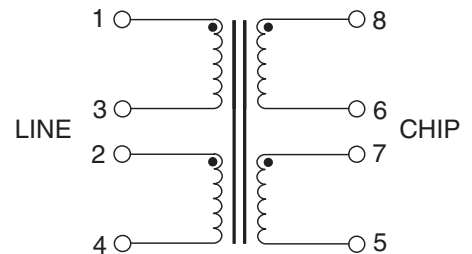
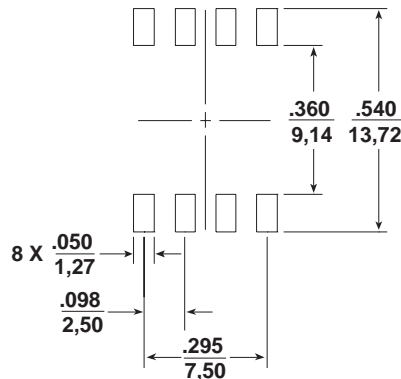
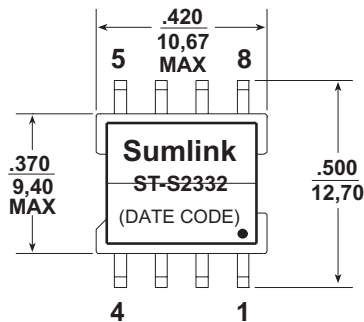


Electrical Specifications @ 25°C — Operating Temperature -40°C to 85°C

Part Number	Turns Ratio Ratio (Line:Chip)	OCL Primary ($\mu\text{H} \pm 6\%$)	Leakage Inductance ($\mu\text{H} \text{ MAX}$)	DCR Primary (1-4 w/2-3 tied) ($\Omega \text{ MAX}$)	DCR Secondary (8-5 w/6-7 tied) ($\Omega \text{ MAX}$)	Isolation Voltage (Vrms)	THD @ 20kHz, 4.5 Vp-p (dB MIN)
ST-S2332	1.41 : 1	900	10.0	4.4	3.5	1500	-80

Mechanical

Schematic



Dimensions: $\frac{\text{Inches}}{\text{mm}}$
 Unless otherwise specified, all tolerances are $\pm \frac{.010}{.025}$